

ATE

4200 Series - JTAG Technologies Boundary Scan Run-Time Option

AEROFLEX
A passion for performance.



- Run-time support for JTAG Technologies Boundary Scan
- Can be fitted to any PC controlled 4200 Series system
- Integrated MTL software interface
- Precise reporting of errors using JTAG Technologies diagnostics
- Logging of fault data for analysis by i-Base5
- Full support for the execution of:
 - Infrastructure tests
 - Interconnect tests
 - Memory cluster tests
 - Functional logic cluster tests
 - CPLD programming
 - SVF
 - JAM / STAPL
 - ISC (IEEE Std.1532)
 - Flash memory programming

The Aeroflex 4200 Series are high performance In-Circuit Test Systems (ICT) capable of performing full Analog and Digital testing of printed circuit boards. This capability also includes an option for basic Boundary Scan tests, which can be used to increase the tester's fault coverage where physical probe access is restricted or where there are insufficient test channels available.

In order to increase the Boundary Scan capability of the 4200 further, an option has been developed that allows the execution of JTAG Technologies Boundary Scan test programs. Using this technique allows regular boundary-scan interconnect testing plus the execution of tests for embedded logic clusters and memories that are inaccessible from the in-circuit fixture to be tested. It also allows Flash memory to be programmed via the boundary scan chain without the requirement for full physical access via the test fixture.

This option will also allow JTAG Technologies test programs developed for engineering or prototype testing to be re-used in high volume production test. New JTAG Technologies test programs can be developed and debugged "off-line" from the 4200 for later integration with the in-circuit program.

The Aeroflex 4200-JTAG Technologies Boundary Scan option uses a JTAG Technologies high-speed JT3717 PCI controller card that is internally mounted in the system controller of the 4200. The controller card is connected via a standard SCSI-2 cable to a fixture-mounted interface pod. The pod provides local buffering of the signals that are applied to the test access port (TAP) of the board under test, ensuring that the tests can be run at the maximum speed possible (up to 40 MHz TCK).

The JT3717 is fitted with 8 M bytes of memory which is sufficient for most applications. For programming of large flash memory devices, the JT3717 can be upgraded by a further 8 M Bytes of memory to give a total of 16 M Bytes.

The 4200-JTAG upgrade kit (Aeroflex part number 88001) includes the following items:

- JTAG Technologies **JT3717/PCI** Datablaster card
- JTAG Technologies **PF2137/13** interface pod
- 2 meter SCSI-2 cable to connect from **JT3717** to pod
- JTAG Technologies hardware licence key and license file
- Set of JTAG Technologies handbooks
- CDROM containing JTAG Technologies Software. This includes:
 - JTAG Technologies run-time software (**PIP2176/DLL**), diagnostic software (**PM3790/BSD**) and **JT37x7** drivers
 - Aeroflex MTL interface library
- Aeroflex MTL interface library software handbook
- Aeroflex password to enable the use of the 4200-JTAG interface software on a single 4200 Series test system

Additional PF2137/13 pods can be purchased as Aeroflex part number 88003.



About JTAG Technologies

JTAG Technologies was created in 1993 by a team of professionals with proven track records in the test industry. Now an independent company, JTAG Technologies specializes in solving physical access problems involved in testing and in system programming printed circuit boards. They do this with products based on the boundary-scan technique - IEEE Standard 1149.1. JTAG Technologies has offices in the Netherlands, China, Germany, Finland, France, Sweden, the United Kingdom and the United States and a global network of authorized distributors.

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Our passion for performance is defined by three attributes represented by these three icons: solution-minded, performance-driven and customer-focused.